## ABSTRACT OF THE DISCLOSURE

The sampling of a pixel signal by a sampling capacitor is effectuated by applying to the sampling capacitor a voltage equal to the corresponding pixel voltage minus the value of the gate-source voltage of a follower transistor biased with a predetermined constant bias current for a first predetermined duration so as to obtain for the sampling capacitor a final state of stable

5    charge. The bias current is then interrupted. Lastly, the end of the sampling pulse occurs on completion of a second predetermined duration after the said interruption of the current.